COMMUNICATION ENGINEERING AND ELECTRONIC TECHNOLOGIES

(Lecce - Università degli Studi)

Insegnamento MICROELECTRONIC DESIGN C.I.

GenCod A005769

Docente titolare STEFANO D'AMICO

Insegnamento MICROELECTRONIC DESIGN C.I.

Insegnamento in inglese MICROELECTRONIC DESIGN C.I.

Settore disciplinare ING-INF/01

Corso di studi di riferimento COMMUNICATION ENGINEERING AND

Tipo corso di studi Laurea Magistrale

Crediti 6.0

Ripartizione oraria Ore Attività frontale: 54.0

Per immatricolati nel 2022/2023

Erogato nel 2022/2023

Anno di corso 1

Lingua

Percorso PERCORSO COMUNE

Sede Lecce

Periodo

Tipo esame Orale

Valutazione

Orario dell'insegnamento https://easyroom.unisalento.it/Orario

BREVE DESCRIZIONE DEL CORSO	The course is aimed at providing principles and tools to analyze and design analog circuits in CMOS integrated technology.
PREREQUISITI	it is recommended to overcome the exam of Analog Electronics preliminary.
OBIETTIVI FORMATIVI	After the course the student should be able to: 1) Describe the basic analog circuits (bandgap reference, current mirrors, differential couple, Miller opamp, class A and class AB output stages, etc...). 2) Evaluate the performance parameters and discuss complexity issues associated with different basic analog circuits. 3) Demonstrate circuit analysis capability of not standard circuits. 4) Understand the technology limits in circuit design. 5) Use the simulator to analyse performance of analog circuits.
METODI DIDATTICI	The Course forecasts 30 hours of theoretical lectures about technology description and fundamental circuit analysis. The theoretical concepts are verified in laboratory by using state of the art circuit simulator. 24 hours of laboratory are forecast.

MODALITA' D'ESAME	The final (oral) exam consists of two cascaded parts: 1. the first part is based on the discussion about a report on the assigned circuit. The circuit must be simulated at the calculator. The student is asked to learn using the simulator, to illustrate the circuit design, to evaluate the performance parameters, and to define the operation of each part of the circuit. it is aimed to verify to what extent the student has gained knowledge and understanding of the use of the circuit simulator and the circuit analysis. 2. the second part is on circuit analysis of one of the basic circuits studied during the course; it is aimed to determine to what extent the student the circuit analysis capability, ability to identify and use data to formulate responses to well-defined problems, problem solving abilities and the capacity integrate different concepts and tools.
ALTRE INFORMAZIONI UTILI	To set an appointment contact prof. Stefano D'Amico by email (stefano.damico@unisalento.it)

PROGRAMMA ESTESO

- The MOS transistor [1,2,3,4,5,6] (6 hours)

-Description of the NMOS transistor
-Second order effects: velocity saturation of carriers and variation of the threshold voltage
-Noise in MOS device
-MOS transistor layout

- Passive components [1,7] (6 hours)

-Integrated capacitors: implementation, accuracy and layout issue
-Integrated resistors: implementation, accuracy and layout issue

- Analog switches [1,8] (6 hours)

-Analog switches implementation
-Charge injection and clock feedtrough

- Bias circuits [1,9,10] (6 hours)

-CMOS current mirrors
-Current reference
-Voltage reference

- Basic gain stages [1,11] (6 hours)

-Gain stages
-Output stages
-Level shifter

- Exercitation

-Analysis and design of circuit examples [1] (6 hours)

- Laboratory

-Design experiences by using the circuit simulator [12] (18 hours):

- Transistor Behaviour:

Coarse MOS parameter extraction
MOS behaviour worst case variation
Channel length modulation effects
Low-voltage current mirror design
VTH dependence on MOS gate length (L)
VTH dependence on MOS gate width (W)
Velocity saturation effects

- Circuit design

A two-stage opamp

TESTI DI RIFERIMENTO

1. Baschirotto, "Slides del corso"
2. S. D'Amico "Chapter 4: The MOS transistor"
3. Johns & Martin "Analog Integrated circuits design", John Wiley and Sons, Inc., pages 7-45.
4. Johns & Martin "Analog Integrated circuits design", John Wiley and Sons, Inc., pages 102-107.
5. Johns & Martin "Analog Integrated circuits design", John Wiley and Sons, Inc., pages 116-130.
6. Johns & Martin "Analog Integrated circuits design", John Wiley and Sons, Inc., pages 187-226.
7. Johns & Martin "Analog Integrated circuits design", John Wiley and Sons, Inc., pages 108-115.
8. Johns & Martin "Analog Integrated circuits design", John Wiley and Sons, Inc., pages 401-451.
9. Johns & Martin "Analog Integrated circuits design", John Wiley and Sons, Inc., pages 131-175.
10. Gray, Hurst, Lewis, Mayer "Analysis and design of integrated circuits" Fourth edition, John Wiley and Sons, Inc. pages 299-332
11. Johns & Martin "Analog Integrated circuits design", John Wiley and Sons, Inc., pages 227-310.
12. Tutorials on CAD exercises